

大同大學 九十二 學年度研究所碩士在職班入學考試

考試科目:科技英文

所別:通訊工程研究所

第 1/1

註:本次考試 不可以參考自己的書籍及筆記; 不可以使用字典; 不可以使用計算器。

Please translate following paragraphs into Chinese

1. Impedance of a one-port microwave device can be determined from measurement on the standing wave at its input. Those required parameters are the VSWR and the location of first minimum from the load. A slotted line that is equipped with a detector probe is connected before the load to facilitate the measurement. Since the output of a detector is proportional to power, the square root of the ratio is taken to find the VSWR.
2. As advances in technology provide increasingly faster and less expensive digital hardware, more of the traditionally functions of a radio receiver will be replaced with software or digital hardware. The final goal for radio receiver design is to directly digitize the RF signal at the output of the receive antenna and therefore implement all receiver function in either digital hardware or software.
3. IP networks traditionally offer "best-effort" service. Different packets could take different routes and could therefore arrive at their destinations with random delays. It is quite possible that this type of routing can cause congestion, or hot spots in parts of the network.
4. For a band-limited signal, if the sampling instants are sufficiently close, then the signal can be reconstructed exactly; i.e., through the use of a lowpass filter, exact interpolation can be carried out between the sample points. The interpretation of reconstruction of $x(t)$ as a process of interpolation becomes evident when we consider the effect in the time domain of a lowpass filter in Figure 7.4.
5. At the functional level simulation, system operation is described in terms of a sequence of data transfers between registers, adders, memories, and other functional units. At the logic level simulation, the system is described in terms of logic elements like gates and flip-flops and their interconnections. Logic level simulation may be used to verify the correctness of the logic design and to analyze the timing.